

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	110	duplicate with SYNC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:09
L4	1	duplicate with SYNC with mask	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:09
L5	2	duplicate with SYNC with mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:10
L6	52	duplicate with SYNC with signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:10
L7	0	duplicate with SYNC with logicsignal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:10
L8	5	duplicate with SYNC with logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:50
L9	2	grooming with SYNC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:21
L10	0	grooming with skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:20



L11	3	grooming same skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:20
L12	2	grooming same SYNC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:21
L13	115	grooming and SYNC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:21
L14	94	grooming and SYNC and logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:23
L15	22	grooming and SYNC and mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:27
L16	10	grooming and duplicate and SYNC and mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 16:28
L21	4220	duplicate and logic and mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:51
L22	1	(duplicate adj logic) with mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:52



L23	1	(duplicate adj logic) same mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:53
L24	31	(duplicate adj logic) and mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:54
L25	11	(duplicate adj logic) and mask\$3 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:57
L26	22	(grooming) and mask\$3 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:57
L27	0	(grooming) with mask\$3 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:57
L28	0	(grooming) same mask\$3 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 17:57
L29	22	(grooming) and mask\$3 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:00
L30	2	(grooming) with sync and mask\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:20



L31	1732	713/400	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:21
L32	2218	clock adj domain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:21
L33	188	31 and 32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:21
L34	49	33 and sync	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:21
L35	2	"5054020".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 18:33
S1	7336	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:00
S2	730	sampl\$4 adj compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 07:59
S3	3	((SYNC adj pulse) and (sampl\$4 adj compensat\$4)) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 08:32
S4	10	(SYNC adj pulse) and (sampl\$4 adj compensat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 08:08



S5	1604	713/400	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 08:09
S6	23	713/400 and (SYNC adj pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:14
S7	1	(sampl\$4 adj compensat\$4) and (713/400 and (SYNC adj pulse))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 08:32
S8	59	713/400 and SYNC and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:17
S9	6253	compensat\$4 and jitter and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:18
S10	90	(compensat\$4 and jitter and delay) and 713/400	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:18
S11	55	((compensat\$4 and jitter and delay) and 713/400) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:19
S12	32	((((compensat\$4 and jitter and delay) and 713/400) and skew) and sampl\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:25
S13	16328	sampl\$5 with compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:27
S14	0	jitter with complenst\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:26



S15	2036	jitter with compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:27
S16	206	(sampl\$5 with compensat\$4) and (jitter with compensat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:27
S17	31	((sampl\$5 with compensat\$4) and (jitter with compensat\$4)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:32
S18	6	((sampl\$5 with compensat\$4) and (jitter with compensat\$4)) and skew) and sync	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:34
S19	5427	sampl\$5 near3 compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:39
S20	1241	jitter near3 compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:39
S21	56	(sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:41
S22	7	((sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)) and SYNC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:47
S23	25	((sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)) and SYNC\$7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:50
S24	88825	clock near3 sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:50



S25	4968	(clock near3 sync\$12) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:51
S26	594	((clock near3 sync\$12) and skew) and sampl\$5 near4 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:52
S27	10	((((clock near3 sync\$12) and skew) and sampl\$5 near4 circuit) and jitter near3 compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:55
S28	585	clock near4 domain near4 sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:56
S29	75	(clock near4 domain near4 sync\$12) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 11:56
S30	40	((clock near4 domain near4 sync\$12) and jitter) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 13:40
S31	3	("4791404"   "6049887"   "6256717").PN.	USPAT	OR	OFF	2004/09/24 13:16
S32	3	"6516362".URPN.	USPAT	OR	OFF	2004/09/24 13:18
S33	3	("4791404"   "6049887"   "6256717").PN.	USPAT	OR	OFF	2004/09/24 13:25
S34	3607	n adj n-1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 13:41
S35	0	((("4791404"   "6049887"   "6256717").PN.) and (n adj n-1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 13:41
S36	1	((clock near4 domain near4 sync\$12) and jitter) and (n adj n-1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 13:58



S37	0	710/400 and sampl\$5 and delay adj compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 14:00
S38	17	713/400 and sampl\$5 and delay adj compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 15:57
S39	7	713/400 and data adj transfer adj synchronization	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 15:58
S40	11166	sampl\$5 with SYNC\$12 with Clock\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 16:30
S41	171	(sampl\$5 with SYNC\$12 with Clock\$1) and delay near3 compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 16:32
S42	51	((sampl\$5 with SYNC\$12 with Clock\$1) and delay near3 compensation) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/24 16:32
S43	534	clockadj skew adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/25 12:07
S44	63	clock adj skew adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/25 12:31
S45	16333	sampl\$6 with compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:06
S46	27090	delay with compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:06



S47	2141	((sampl\$6 with compensat\$5) and (delay with compensat\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:07
S48	153	((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:07
S49	138123	((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) ad jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:07
S50	63	((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:32
S51	25	((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:44
S52	3	sampling adj compensation adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:45
S53	36	sampling adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:48
S54	2	(sampling adj compensation) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:48
S55	2	(sampling adj compensation) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:49
S56	7	(sampling adj compensation) and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:52



S57	7340	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:35
S58	285	(SYNC adj pulse) and plurality with (zero\$1 or low\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:53
S59	14	((SYNC adj pulse) and plurality with (zero\$1 or low\$1)) and insert with (one\$1 or high\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 08:54
S60	59	(((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:04
S61	59	(((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:06
S62	56	((((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12) and skew) and clock near4 signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:23
S63	7340	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:23
S64	371	sampl\$5 near compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:53
S65	6	(SYNC adj pulse) and (sampl\$5 near compensation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:42
S66	24589	clock near first	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:44



S67	484	(SYNC adj pulse) and (clock near first)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:45
S68	123	((SYNC adj pulse) and (clock near first)) and plurality and (lows or zeros)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:46
S69	670	plurality with (lows or zeros)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:47
S70	2	((SYNC adj pulse) and (clock near first)) and (plurality with (lows or zeros))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:47
S71	560	sampl\$5 with SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:53
S72	25	(sampl\$5 with SYNC adj pulse) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:58
S73	484	(SYNC adj pulse) and (clock near first)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:59
S74	65	((SYNC adj pulse) and (clock near first)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 10:59
S75	3882	phase adj align\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 12:44
S76	319	(phase adj align\$5) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 12:44



S77	125	((phase adj align\$5) and skew) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 12:45
S78	33	(clock near first) and (((phase adj align\$5) and skew) and jitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 12:45
S79	99	(SYNC adj pulse) and (phase adj align\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:14
S80	16	((SYNC adj pulse) and (phase adj align\$5)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:14
S81	12522	data and transfer and clock\$1 and domain\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:36
S82	331	(SYNC adj pulse) and (data and transfer and clock\$1 and domain\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:36
S83	1081	(data near2 transfer) and (clock\$1 near2 domain\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:37
S84	42	(SYNC adj pulse) and ((data near2 transfer) and (clock\$1 near2 domain\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:47
S85	3	sync adj pulse adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:48
S86	31	sync adj pulse with compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:52



S87	945	sync adj pulse and compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:53
S88	8	(sync adj pulse) near3 compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/27 14:53
S89	7405	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S90	747	sampl\$4 adj compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S91	3	((SYNC adj pulse) and (sampl\$4 adj compensat\$4)) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S92	11	(SYNC adj pulse) and (sampl\$4 adj compensat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S93	1690	713/400	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S94	27	713/400 and (SYNC adj pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S95	1	(sampl\$4 adj compensat\$4) and (713/400 and (SYNC adj pulse))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S96	63	713/400 and SYNC and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S97	6444	compensat\$4 and jitter and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S98	96	(compensat\$4 and jitter and delay) and 713/400	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S99	60	((compensat\$4 and jitter and delay) and 713/400) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 0	35	((((compensat\$4 and jitter and delay) and 713/400) and skew) and sampl\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 1	16662	sampl\$5 with compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 2	0	jitter with complenst\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 3	2089	jitter with compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 4	216	(sampl\$5 with compensat\$4) and (jitter with compensat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 5	32	((sampl\$5 with compensat\$4) and (jitter with compensat\$4)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 6	7	((((sampl\$5 with compensat\$4) and (jitter with compensat\$4)) and skew) and sync	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:28



S10 7	5556	sampl\$5 near3 compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 8	1263	jitter near3 compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S10 9	60	(sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 0	8	((sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)) and SYNC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 1	27	((sampl\$5 near3 compensat\$5) and (jitter near3 compensat\$5)) and SYNC\$7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 2	90316	clock near3 sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 3	5097	(clock near3 sync\$12) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 4	614	((clock near3 sync\$12) and skew) and sampl\$5 near4 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 5	10	((((clock near3 sync\$12) and skew) and sampl\$5 near4 circuit) and jitter near3 compensat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 6	626	clock near4 domain near4 sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S11 7	90	(clock near4 domain near4 sync\$12) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 8	53	((clock near4 domain near4 sync\$12) and jitter) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S11 9	3	("4791404"   "6049887"   "6256717").PN.	USPAT	OR	OFF	2004/11/23 15:16
S12 0	4	"6516362".URPN.	USPAT	OR	OFF	2004/11/23 15:16
S12 1	3	("4791404"   "6049887"   "6256717").PN.	USPAT	OR	OFF	2004/11/23 15:16
S12 2	3693	n adj n-1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 3	0	((("4791404"   "6049887"   "6256717").PN.) and (n adj n-1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 4	2	((clock near4 domain near4 sync\$12) and jitter) and (n adj n-1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 5	0	710/400 and sampl\$5 and delay adj compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 6	21	713/400 and sampl\$5 and delay adj compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 7	7	713/400 and data adj transfer adj synchronization	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S12 8	11328	sampl\$5 with SYNC\$12 with Clock\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S12 9	173	(sampl\$5 with SYNC\$12 with Clock\$1) and delay near3 compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 0	51	((sampl\$5 with SYNC\$12 with Clock\$1) and delay near3 compensation) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 1	546	clockadj skew adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 2	66	clock adj skew adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 3	16667	sampl\$6 with compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 4	27582	delay with compensat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 5	2195	(sampl\$6 with compensat\$5) and (delay with compensat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 6	157	((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 7	141441	((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) ad jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S13 8	64	((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S13 9	25	(((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 0	3	sampling adj compensation adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 1	36	sampling adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 2	2	(sampling adj compensation) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 3	2	(sampling adj compensation) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 4	7	(sampling adj compensation) and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 5	7405	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 6	288	(SYNC adj pulse) and plurality with (zero\$1 or low\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 7	14	((SYNC adj pulse) and plurality with (zero\$1 or low\$1)) and insert with (one\$1 or high\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S14 8	60	(((((sampl\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S14 9	60	(((((saml\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 0	57	(((((saml\$6 with compensat\$5) and (delay with compensat\$5)) and skew) and jitter) and sync\$12) and skew) and clock near4 signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 1	7405	SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 2	377	saml\$5 near compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 3	6	(SYNC adj pulse) and (saml\$5 near compensation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 4	25033	clock near first	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 5	501	(SYNC adj pulse) and (clock near first)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 6	131	((SYNC adj pulse) and (clock near first)) and plurality and (lows or zeros)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 7	681	plurality with (lows or zeros)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S15 8	2	((SYNC adj pulse) and (clock near first)) and (plurality with (lows or zeros))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S15 9	574	sample\$5 with SYNC adj pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 0	31	(sample\$5 with SYNC adj pulse) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 1	501	(SYNC adj pulse) and (clock near first)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 2	78	((SYNC adj pulse) and (clock near first)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 3	3969	phase adj align\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 4	328	(phase adj align\$5) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 5	128	((phase adj align\$5) and skew) and jitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 6	34	(clock near first) and (((phase adj align\$5) and skew) and jitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 7	103	(SYNC adj pulse) and (phase adj align\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S16 8	16	((SYNC adj pulse) and (phase adj align\$5)) and skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16



S16 9	12967	data and transfer and clock\$1 and domain\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 0	355	(SYNC adj pulse) and (data and transfer and clock\$1 and domain\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 1	1150	(data near2 transfer) and (clock\$1 near2 domain\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 2	62	(SYNC adj pulse) and ((data near2 transfer) and (clock\$1 near2 domain\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 3	3	sync adj pulse adj compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 4	31	sync adj pulse with compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 5	963	sync adj pulse and compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 6	8	(sync adj pulse) near3 compensation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/23 15:16
S17 7	602	jitter near delay and synchroniz\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:01
S17 8	16	S177 and clock adj1 skew	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:05



S17 9	1	S178 and first adj1 clock and second adj1 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:02
S18 0	637	(series with delay with circuit) and skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 08:29
S18 1	36	(series with delay with circuit) with skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 08:37
S18 2	0	(series with delay with circuit with eight) with skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 08:37
S18 3	1	(series with delay with circuit with "8") with skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:12
S18 4	0	detct with lows with high	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:12
S18 5	15968	detect with lows with high	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:12
S18 6	26	detect with lows with high with insert	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:12
S18 7	0	detect with lows with (insert near2 hight)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:13



S18 8	2	detect with (low\$1 or zero\$1) with (insert near2 (hight or one))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:13
S18 9	2	detect with (low\$1 or zero\$1) with (insert near2 (hight or one))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:16
S19 0	0	"one inser circuit"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:15
S19 1	1	"one insert circuit"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:15
S19 2	74240	detect with (low\$1 or zero\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:16
S19 3	8581	logic adj low\$1 with logic adj high\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:17
S19 4	8581	(logic adj low\$1) with (logic adj high\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:17
S19 5	31	(logic adj low\$1) with (insert\$4 with (logic adj high\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:29



S19 6	723802	(insert\$4 with (one or (logic adj high\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:30
S19 7	1279	(logic adj low\$1 or zero\$1) with (insert\$4 near4 (one or (logic adj high\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:32
S19 8	732	(logic adj low\$1 or zero\$1) with (insert\$4 adj4 (one or (logic adj high\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:32
S19 9	384	(logic adj low\$1 or zero\$1) with (insert\$4 adj2 (one or (logic adj high\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:32
S20 0	212	(logic adj low\$1 or zero\$1) with (insert\$4 adj1 (one or (logic adj high\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:33
S20 1	5	(logic adj low\$1 or zero\$1) with (insert\$4 adj1 (one or (logic adj high\$1))) and jitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:34
S20 2	0	S200 and shychronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:35
S20 3	0	S200 and shychro\$10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 10:34

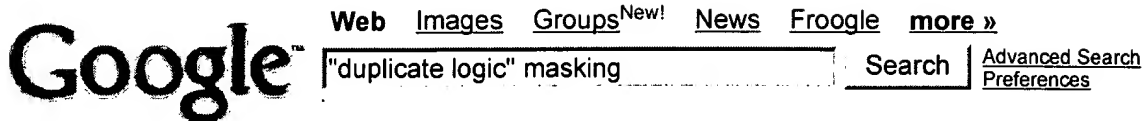


S20 4	33	S200 and synchronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:28
S20 5	356	(second adj clock) with PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:28
S20 6	173	S205 and synchronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:53
S20 7	18	S205 and (sync adj pulse)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:43
S20 8	2	"6114917".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:44
S20 9	2	"4929918".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:45
S21 0	0	"JP 10-322200"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:45
S21 1	2	"JP 10322200"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:47



S21 2	1	"09/944500"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:47
S21 3	17	S205 and (clock with domain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 12:53
S21 4	0	"duplicat5e logic hight"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:39
S21 5	0	"duplicate logic hight"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:39
S21 6	1	"duplicate logic high"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:39
S21 7	179	"duplicate logic"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:39
S21 8	0	(duplicate adj logic) with hih	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:40
S21 9	8	(duplicate adj logic) with high	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 13:40





## Web

Results 1 - 9 of about 37 for "**duplicate logic**" **masking**. (0.06 seconds)

### [PPT] VHDL Synthesis for High-Reliability Systems

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... Better to explicitly **duplicate logic** in code. ... **Masking** or recovering from erroneous conditions in a system once they have been detected. ...

[klabs.org/mapld04/tutorials/vhdl/presentations/methodologies.ppt](http://klabs.org/mapld04/tutorials/vhdl/presentations/methodologies.ppt) - [Similar pages](#)

### Class Definition for Class 326 - ELECTRONIC DIGITAL LOGIC ...

... class (326), the security is performed by disabling or **masking** the circuit ... matter wherein the logic circuit comprises at least one **duplicate logic** stage which ...

[www.uspto.gov/go/classification/uspc326/defs326.htm](http://www.uspto.gov/go/classification/uspc326/defs326.htm) - 101k - [Cached](#) - [Similar pages](#)

### [PDF] April 2001 CLASSIFICATION DEFINITIONS 326 - 1 CLASS 326 ...

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... this class (326), the security is performed by disabling or **masking** the circuit ... wherein the logic circuit comprises at least one **duplicate logic** stage which ...

[www.uspto.gov/go/classification/uspc326/defs326.pdf](http://www.uspto.gov/go/classification/uspc326/defs326.pdf) - [Similar pages](#)

### Mimic WIP

... So I get the MegaCD bios booting and then get distracted by other things :) I added sprite **masking** to the Megadrive ... This cleans up a lot of **duplicate logic**. ...

[mimic.arcadeheaven.com/](http://mimic.arcadeheaven.com/) - 20k - [Cached](#) - [Similar pages](#)

### [PDF] Microsoft PowerPoint - Chameleon\_011603.ppt

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... backup to do stage-wise shutdown System Boards – Self-checking by having independent **duplicate logic** run synchronously ... Fault **Masking** Using Replication ...

[shay.ecn.purdue.edu/~ece572/handouts/HwSw\\_FTEexamples\\_011504.pdf](http://shay.ecn.purdue.edu/~ece572/handouts/HwSw_FTEexamples_011504.pdf) - [Similar pages](#)

### Patent 4371949: Time-shared, multi-phase memory accessing system ...

... 7. From the foregoing description of the rotation, **masking**, merging and writing ... register 314 responsive to a syndrome decoder 320 and **duplicate logic** 321, and ...

[www.freepatentsonline.com/4371949.html](http://www.freepatentsonline.com/4371949.html) - 75k - Supplemental Result - [Cached](#) - [Similar pages](#)

### Software Bringup

... change. To eliminate some **duplicate logic**, I created a new signal, \_WR - memory write. ... I suspect a problem with interrupt **masking**. Some ...

[www.homebrewcpu.com/software\\_bringup.htm](http://www.homebrewcpu.com/software_bringup.htm) - 101k - [Cached](#) - [Similar pages](#)

### \$Id: NEWS,v 1.714 2002/10/12 22:35:42 tom Exp \$ This is a log of ...

... **duplicate logic** used to initialize trace in newterm(), in initscr() to avoid confusing trace of ... change CharOf() macro to use it for **masking** A\_CHARTEXT data ...

[rebma.cit.cornell.edu/~bs235/bogstaller/ncurses-5.3/NEWS](http://rebma.cit.cornell.edu/~bs235/bogstaller/ncurses-5.3/NEWS) - 101k - Supplemental Result - [Cached](#) - [Similar pages](#)

### United States Patent Application: 0040215799

... to traverse dissimilar networks, hardware, and carriers by effectively **masking** the those ... 4 is the redundant **duplicate logic** for the reverse path receiver 498 ...

[appft1.uspto.gov/.../method&RS=ABST/method](http://appft1.uspto.gov/.../method&RS=ABST/method) - 68k - Supplemental Result - [Cached](#) - [Similar pages](#)

*In order to show you the most relevant results, we have omitted some entries very similar to the 9 already displayed.*

*If you like, you can repeat the search with the omitted results included.*



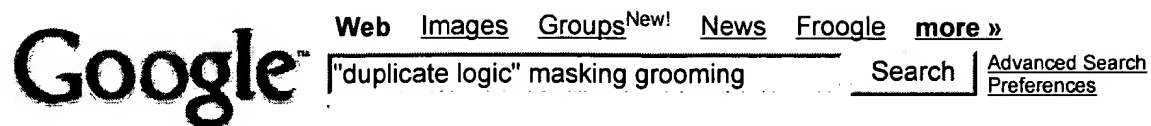
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